REMARKS

Claims 1-29 remain in the application for consideration of the Examiner.

Reconsideration and withdrawal of the outstanding rejections are respectfully requested in light of the following remarks.

Turning now to the art rejection, Claims 1-5, 11-15, 18-21, 24, 25, 28, and 29 were rejected under 35 U.S.C. § 103 as being unpatentable over Matsushita in view of Orndorff; and Claims 6-10, 16-17, 22, 23, 26, and 27 were rejected under 35 U.S.C. § 103 as being unpatentable over Matsushita in view of Orndorff and further in view of Midya.

These rejections are respectfully traversed.

Matsushita does not disclose or suggest the presently claimed invention including a programmable controller operational in response to user selected frequency data to generate control data bits in independent Claim 1, the programmable controller operational in response to user selected frequency information to generate control data bits in independent Claim 12, the programmable programming means for generating control data bits in response to user selected input frequency information in independent Claim 18, albeit defined as the method step of communicating user selected input frequency data to the controller such that the controller generates control data bits determined by the user selected input frequency data in independent Claim 25.

Applicants agree with the Examiner that Matsushita does not disclose this aspect.

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It is respectfully submitted that Orndorff does not disclose or suggest the presently claimed invention including the programmable controller operational in response to the user selected input frequency data to generate control data bits in the various forms in independent Claims 1, 12, 18, and 25.

The Examiner alleges that Orndorff discloses a programmable controller operational in response to user selected input data to generate control data bits.

Notwithstanding the allegations of the Examiner, the Examiner's attention is directed to column 3, lines 9-15 where Orndorff discloses that Figure 3 illustrates the meter circuitry in block diagram form. The above described function key 16 and spin knob 14 provide inputs to the microcontroller 30 which, among other things as will be described, provides control signals to a three-stage attenuator and the first and second local oscillator phased-lock loop ICs 34 and 36.

Nothing in Orndorff relates to frequency. Orndorff does not describe the function of key 16 or spin knob 14 in terms of the function that they provide.

The receive spurs or the look-up tables are not user selected.

Midya does not cure the above noted defects.

In light of the above, it is respectfully submitted that the present application is in condition for allowance, and notice to that effect is respectfully requested.

While it is believed that the instant response places the application in condition for allowance, should the Examiner have any further comments or suggestions, it is respectfully requested that the Examiner contact the undersigned in order to expeditiously resolve any outstanding issues.

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To the extent necessary, Applicant petitions for an Extension of Time under 37 CFR 1.136. Please charge any fees in connection with the filing of this paper, including extension of time fees, to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,
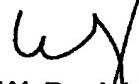
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